

1. High Reliability Soldering in Semiconductor Packaging

Course Leader: Ning-Cheng Lee – ShinePure Hi-Tech

Course Description:

Semiconductor soldering is much more delicate and is very critical for reliability of devices. This course covers the critical parameters governing the reliability for soldering in semiconductor packaging. The reliability discussed includes parameters affecting the intermetallic compounds (IMC), voiding, electromigration, low temperature soldering, high temperature soldering, and electrochemical migration under a variety of material combinations. The failure modes are discussed in details, with preferred choices of materials and designs recommended.

Course Outline:

1. IMC -Effect of Cu Pad Grain Size on IMC
2. IMC - Interaction of Cu and Ni
3. IMC - Effect of Base Metal Co-P on IMC
4. Voiding - Effect of Solder Form on Voiding
5. Voiding - Effect of Joint Height, Temperature, Electrical, Mechanical on Kirkendall Voiding
6. Voiding - Effect of Cu Structure on Kirkendall Voids
7. Electrochemical Migration (ECM)
8. Electromigration
9. LTS - Bi-Rich Whisker Growth
10. LTS - TCT Reliability of LTS
11. LTS - Collapse of LTS
12. LTS - Deposition, Hot Tear, Bi Stratification of LTS
13. LTS - Hot Tear of Homogeneous LTS BiSn - Effect of Profile
14. LTS - Drop Test of LTS
15. HTS - TLPB (Transient Liquid Phase Bonding)

Who Should Attend:

Anyone who cares about achieving high reliability solder joints for semiconductor packaging and wants to know how to achieve it should take this course

Bio: Ning-Cheng Lee is founder of ShinePure Hi-Tech. Prior to that, he has been with Indium from 1986 to 2021. Prior to joining Indium, he was with Morton Chemical and SCM. He has more than 30 years of experience in the development of fluxes and solder materials for SMT Industries. He received his PhD in polymer science from University of Akron in 1981, and BS in chemistry from National Taiwan University in 1973. Ning-Cheng is the author of “Reflow Soldering Processes and Troubleshooting: SMT, BGA, CSP, and Flip Chip Technologies” by Newnes, and co-author of 5 other books. He received the 1991 award from SMT Magazine and 1993 and 2001 awards for best proceedings papers of SMI or SMTA International Conferences, 2003 Lead Free Co-Operation Award from Soldertec, 2008 and 2014 awards from IPC for Honorable Mention Paper – USA Award of APEX conference, and 2010 Best Paper Award of SMTA China South Conference. He was honored as 2002 Member of Distinction from SMTA, 2006 Exceptional Technical Achievement Award from CPMT, 2007 Distinguished Lecturer from CPMT, 2009 Distinguished Author from SMTA, 2010 Electronics Manufacturing Technology Award from CPMT, 2015 Founder’s Award from SMTA, and 2017 IEEE Fellow.